Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office							Atty Docket No.			Application No.		
							25503/81401			10/665,080		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT							Applicants: Kyung-Oun Jang, et al.					
(Use several sheets if necessary)							ng Date			Group		
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Examiner	St. 6	U.S. Patent Documents  Document						1		Filing C	ate	
_Initial		lumber	Date		Name		Class	ss Subclass		If Appropriate		
JATM		,731,728	Mar. 24, 19		Greiss					<del></del>		
JATM	5	,736,893	Apr. 7, 199		Puckette, et al.			<u> </u>				
JATM	6	,107,851	Aug. 22, 20	000	Balakirshnan, et a	al.						
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JATM Alan B. Grebene, "Analog Integrated Circuit Design," Van Nostrand Reinhold Company (Microelectronics Series), (1972), Page 154.												
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Examiner: /Juan Torres/ Date Considered: 12/14/2006												
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.												

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